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ATTORNEY DOCKET NO. 21812-010210US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

COPY

Inventor(s): Junghyun Kim et al.

Serial No.: 10/749,810

Examiner: Henry Choe

Filing Date: December 30, 2003

Group Art Unit: 2817

Title: MULTIPLE POWER MODE AMPLIFIER WITH BIAS MODULATION OPTION AND WITHOUT
BYPASS SWITCHES

COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria VA 22313-1450

TRANSMITTAL LETTER FOR RESPONSE/AMENDMENT

Sir:

Transmitted herewith is/are the following in the above-identified application:

- ☒ Response/Amendment ☐ Petition to extend time to respond
☐ New fee as calculated below ☐ Supplemental Declaration
☒ No additional fee (Address envelope to "Mail Stop Amendments")
☐ Other: (Fee \$ _____)

CLAIMS AS AMENDED BY OTHER THAN A SMALL ENTITY						
(1) FOR	(2) CLAIMS REMAINING AFTER AMENDMENT	(3) NUMBER EXTRA	(4) HIGHEST NUMBER PREVIOUSLY PAID FOR	(5) PRESENT EXTRA	(6) RATE	(7) ADDITIONAL FEES
TOTAL CLAIMS	127	MINUS	127	= 0	X 50	\$ 0
INDEP. CLAIMS	5	MINUS	5	= 0	X 200	\$ 0
<input type="checkbox"/> FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM					+ 360	\$ 0
EXTENSION FEE	1 ST MONTH 120.00 <input type="checkbox"/>	2 ND MONTH 450.00 <input type="checkbox"/>	3 RD MONTH 1020.00 <input type="checkbox"/>	4 TH MONTH 1590.00 <input type="checkbox"/>		
OTHER FEES						\$ 0
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT						\$ 0

Charge \$ 0 to Deposit Account 50-1078. At any time during the pendency of this application, please charge any fees required or credit any over payment to Deposit Account 50-1078 pursuant to 37 CFR 1.25. Additionally please charge any fees to Deposit Account 50-1078 under 37 CFR 1.16, 1.17, 1.19, 1.20 and 1.21. A duplicate copy of this transmittal letter is enclosed.

Respectfully submitted,

Junghyun Kim et al.

By

Herbert R. Schutze
Attorney/Agent for Applicant(s)

I hereby certify that this paper is being facsimile
transmitted to the Patent and Trademark Office on
the date shown below:

Date of facsimile: June 23, 2005

Typed Name: Linda A. Iimura

Signature:

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Date: June 23, 2005

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I hereby certify that this correspondence is being transmitted via facsimile to the Commissioner for Patents at (703) 872 9306 on June 23, 2005.

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By Linda A. Iimura Date June 23, 2005
Linda A. Iimura

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Inventor(s): Junghyun Kim et al.

Group Art Unit: 282817

Serial No.: 10/374,169749,810

Examiner: Henry Choe

Filed: December 30, 2003

Title: Multiple Power Mode Amplifier with Bias Modulation Option and Without Bypass Switches

Atty Docket: 21812-010210US

ELECTION

Commissioner for Patents
P.O. Box 1450
Alexandria
VA 22313-1450

SIR:

In response to the Office Action mailed May 23, 2005, applicants elect Species VI (Figure 12) for prosecution on the merits.

The following claims are readable on this figure: 1, 3-8, 11, 13-18, 21-47, 53-80, 86-87, 89-94, 97-99, and 105-125.

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Applicants respectfully traverse the restriction requirement.

Initially, applicants note that Figure 6 was identified as a separate species. This is believed to be in error because Figure 6 is merely a more detailed view of a portion of Figure 5. Figure 6 contains no elements not in Figure 5. Therefore, Figure 6 should not be classified as a separate species.

Claims 1, 21 and 87 are believed to be generic to all species. Claim 1 includes the following elements (paraphrased):

- a first branch comprising a first amplifier between first and second nodes,
- a second branch comprising a first matching unit and a second amplifier between second and third nodes,
- a third branch comprising a transformer unit between the second and third nodes,
- in a first mode, second amplifier is off, and
- in a second mode, second amplifier is on.

Each item listed as an element of claim 1 is present in each of Figures 5, 9, 10, 11, 12, 14, 24 and 25 as follows:

- first amplifier 100 (Figs 5, 10, 12, 24 and 25), 110 (Figs 9, 11, 14)
- first node 70 (all figures)
- second node 72 (all figures)
- first matching unit 140 (all figures)
- second amplifier 120 (all figures)
- third node 76 (all figures)
- transformer unit 170 (all figures)
- second amplifier can be on or off

Claim 21 includes the following elements (paraphrased):

- first transistor between input node and first node,
- first block between first and second nodes,
- second block between second and third nodes,
- second transistor between third and fourth nodes,
- third block between fourth and fifth nodes,
- fourth block between second and fifth nodes,
- in a first mode, signal goes through first transistor and first and fourth blocks, and
- in a second mode, signal goes through first transistor; first and second blocks, second transistor, and third block.

Each item listed as an element of claim 21 is present in each of Figures 5, 9, 10, 11, 12, 14, 24, and 25 as follows:

- first transistor 100 (Figs 5, 10, 12, 24 and 25), 110 (Figs 9, 11, 14)
- input node 70 (all figures)
- first node (not numbered. This is the connection between elements 110 and 130 (Figs 9, 11 and 14), and between elements 100 and 130 (all other figures)
- first block 130 (all figures)
- second node 72 (all figures)
- second block 140 (all figures)
- third node 74 (all figures)
- second transistor 120 (all figures)
- fourth node (not numbered. This is the connection between elements 120 and 150 in all figures)
- third block 150 (all figures)
- fifth node 76 (all figures)
- fourth block 170 (all figures)
- signal goes through first transistor and first block, and either fourth block or second block, second transistor and third block.

Claim 87 includes the following elements (paraphrased):

- first branch between first and second nodes comprising N (0 or more) amplifiers,
- second branch between second and third nodes comprising M (1 or more) amplifiers,
- third branch between second and third nodes comprising a transformer unit,
- in a first mode, at least one of M amplifiers is off, and
- in a second mode, the M amplifiers are on.

Each item listed as an element of claim 87 is present in each of Figures 5, 9, 10, 11, 12, 14, 24 and 25 as follows:

- N (0 or more) amplifiers 100 (Figs 5, 10, 12, 24 and 25), 110 (Figs 9, 11, 14),
- first node 70 (all figures),
- second node 72 (all figures),
- M (1 or more) amplifiers 120 (all figures),
- third node 76 (all figures),
- transformer unit 170 (all figures), and
- the M amplifiers can be on or off.

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For the foregoing reasons, the applicants submit that restriction is not required. However, if the Examiner is still of the opinion that restriction to species VI (Figure 12) is required, please cancel claims 2, 9, 10, 12, 19, 20, 48-52, 81-85, 88, 95, 96, 100-104, 126 and 127.

Respectfully submitted,

Junghyun Kim et al.

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Dated: June 23, 2005

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